

The diagram illustrates a three-stage pipeline. The top stage is divided into two sections: 'RE' (Register File) on the left and 'RM' (Read Memory) on the right. Below the 'RE' section is a smaller box labeled 'SP' (Store Processor). The 'RM' section contains two small circles representing data paths. Two vertical red lines are positioned below the 'RM' section, indicating a data path or output.

